



北京大学
PEKING UNIVERSITY

工学院
College of Engineering

CIRCUITS AND ELECTRONICS CMOS

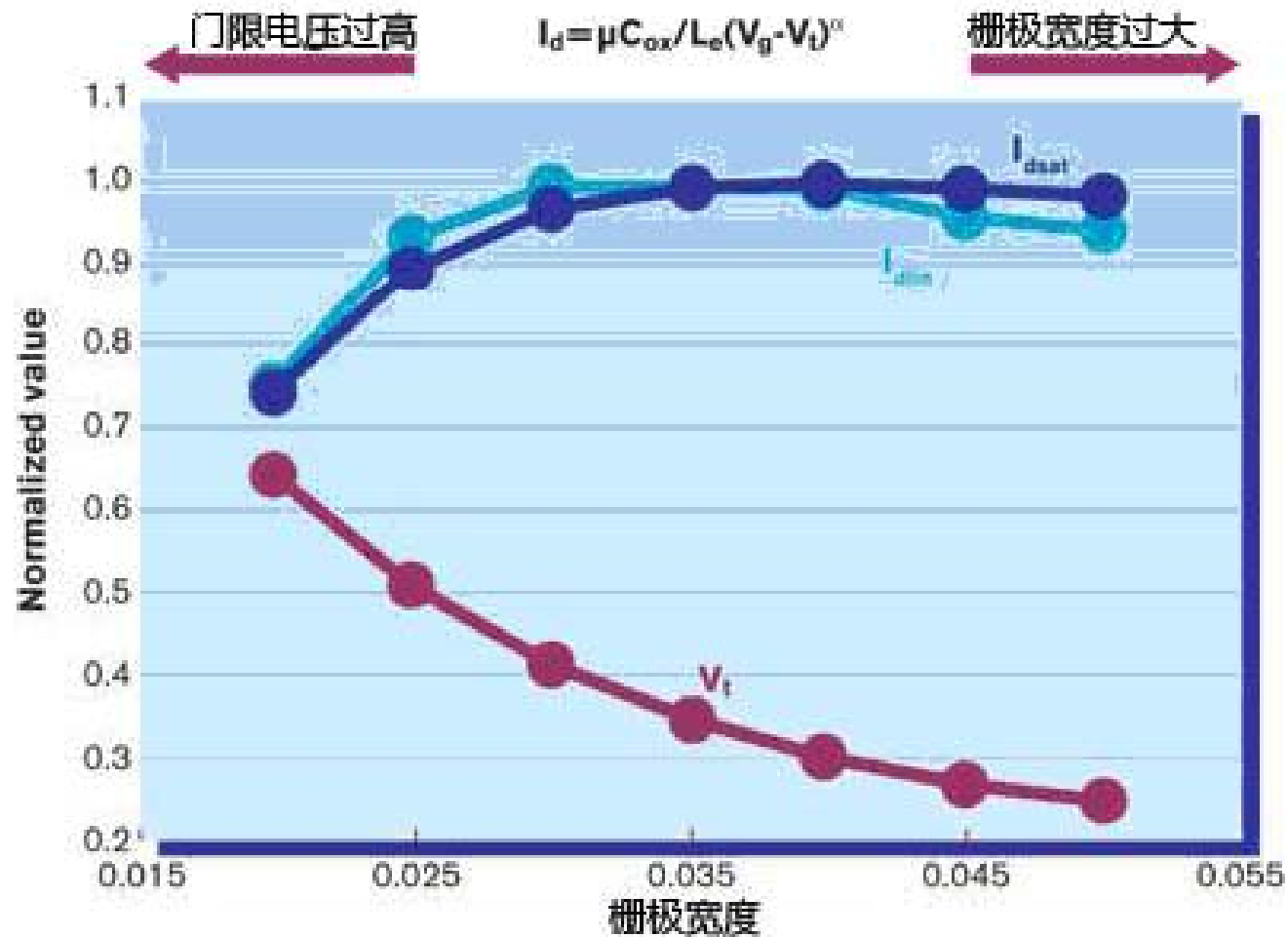
Dr Xun Huang
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Dynamics and Control Group
Spring Semester 2010



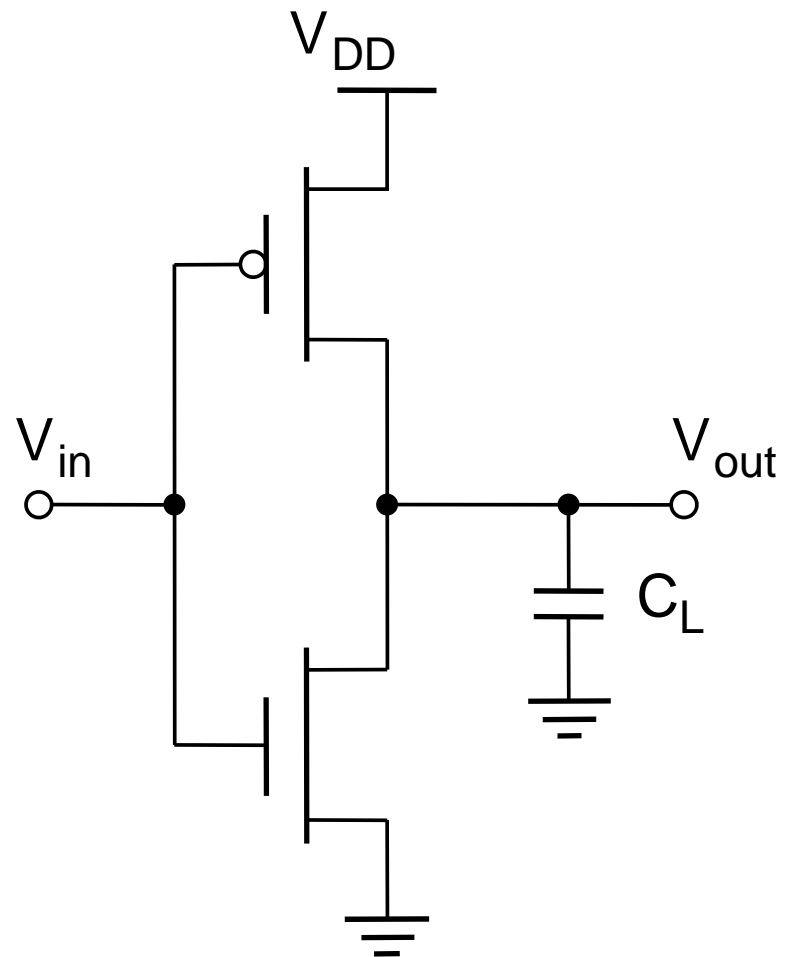
Scale

Scaling Gate Length



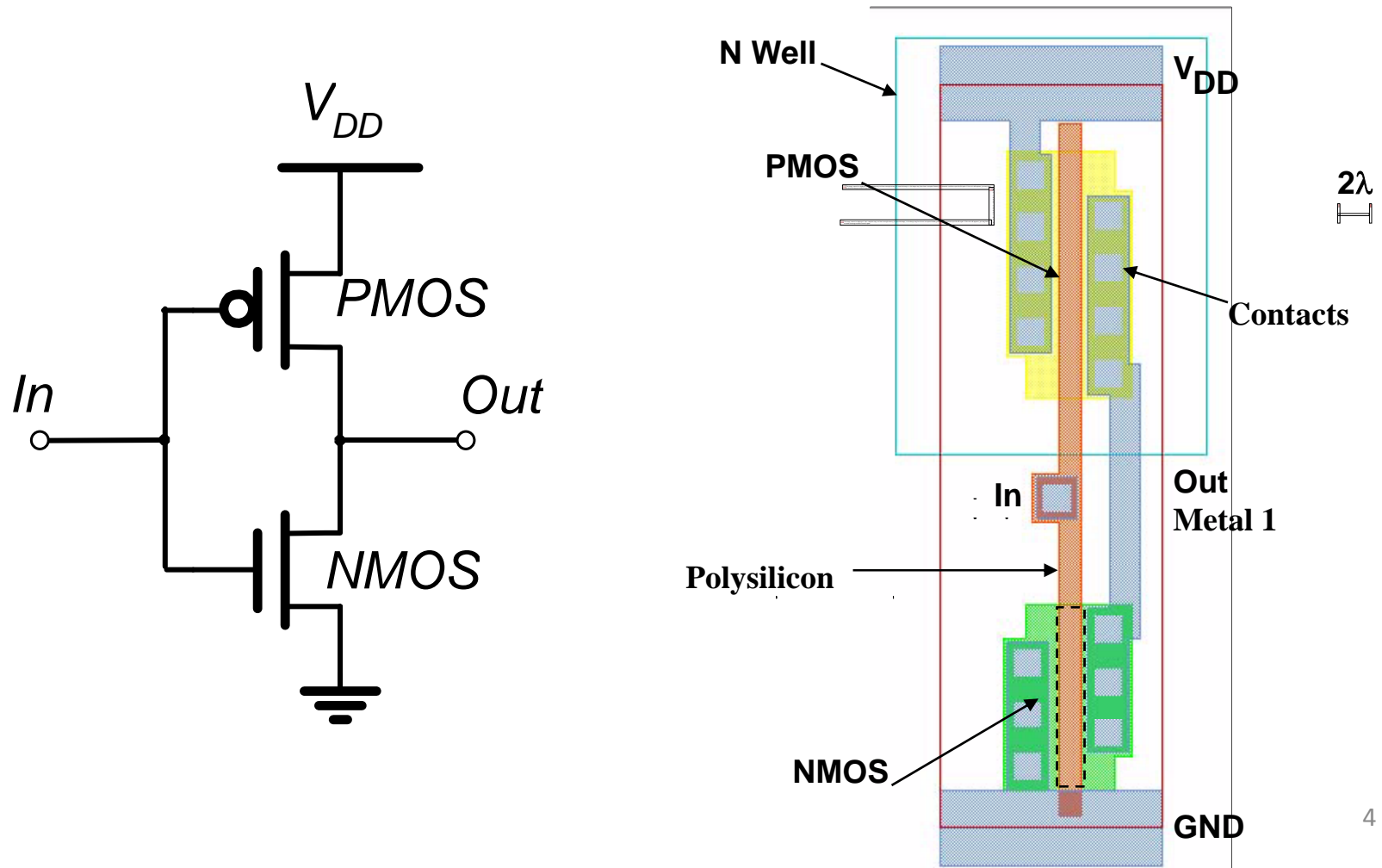


CMOS Inverter





CMOS Inverter: Design Perspective

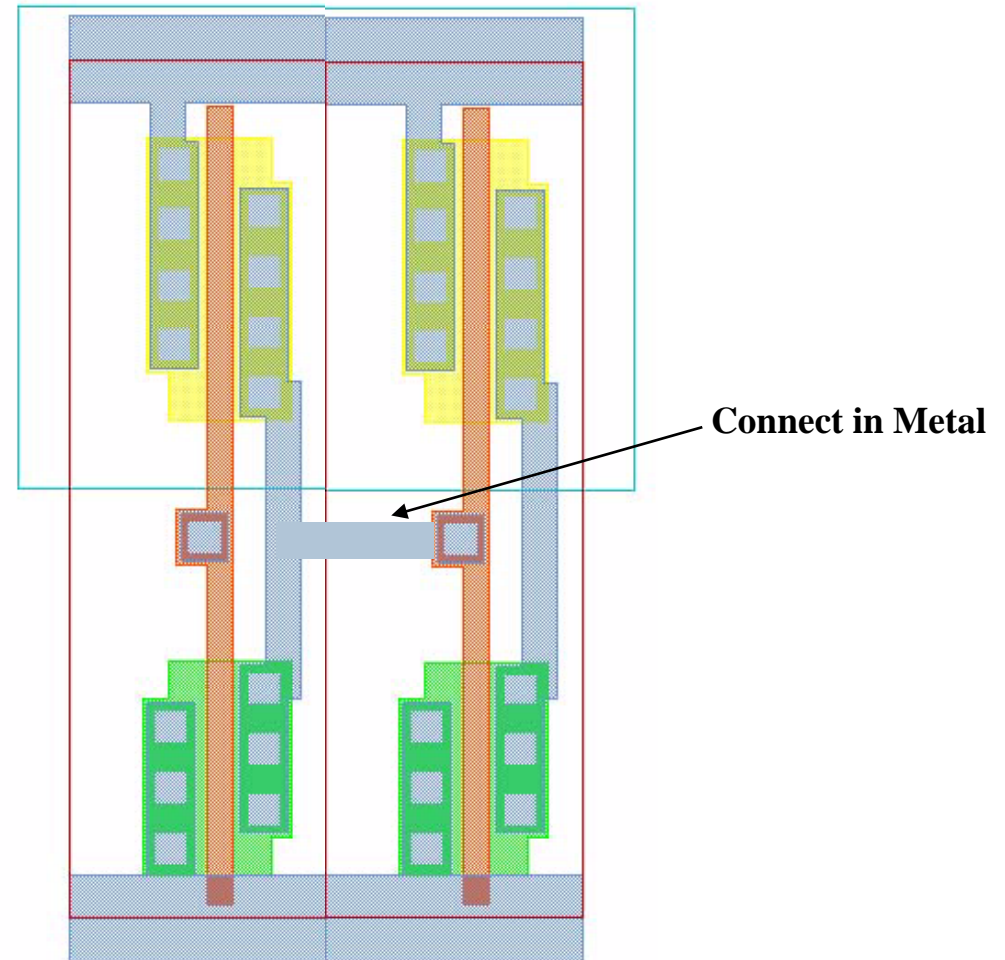
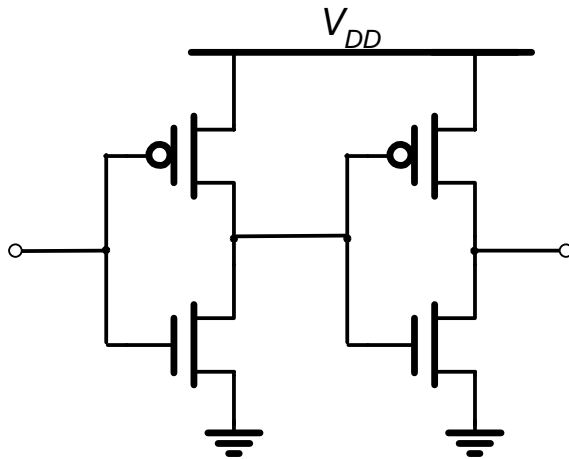




Two CMOS Inverter

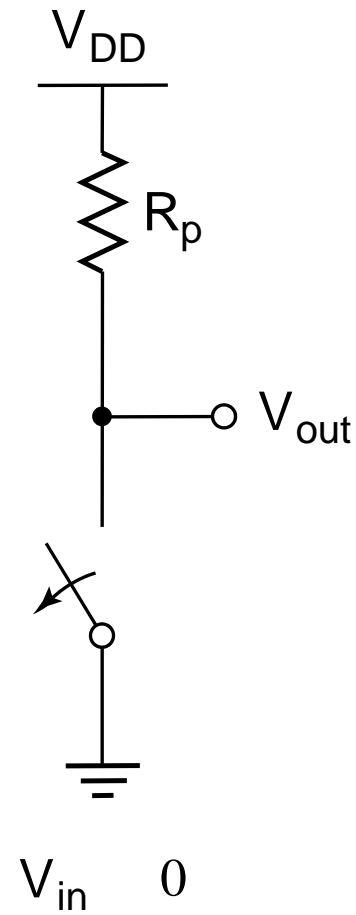
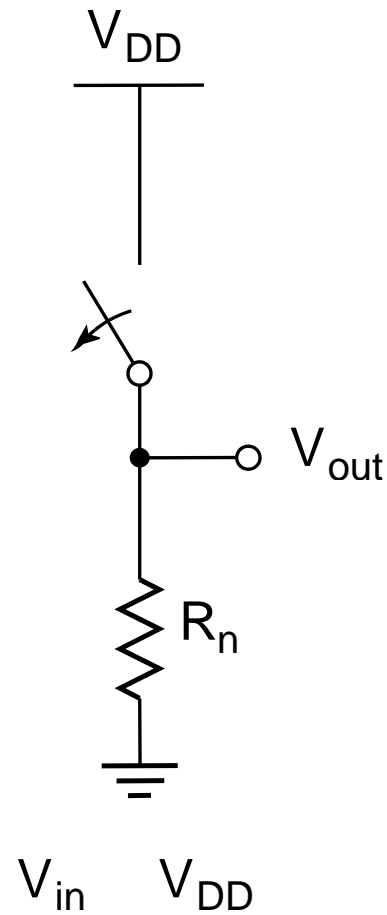
Share power and ground

Abut cells





CMOS Inverter: SR Model





CMOS V-I Characteristics

